IN THE CLAIMS:

1. (Currently Amended) A method of forming at least one quantum dot on a predetermined area of a substrate, comprising:

forming a nucleation site comprising at least one surface or subsurface defect at the predetermined area of the substrate by implantation with ions using a focused ion beam wherein an electronic microscope is used to align said ion beam on said predetermined area of said substrate; and

growing a quantum dot on the nucleation site.

2. (Original) The method of claim 1, wherein the quantum dot is formed on the nucleation site by strained layer epitaxy.

3. (Canceled).

4. (Currently Amended) The method of claim 1 ~~3~~, wherein the ions are selected from a group consisting of gallium, silicon and gold ions.

5. (Original) The method of claim 4, wherein the gallium ions are implanted using a beam energy in a range of about 1 keV to about 50 keV, a beam current of about 10pA, and an exposure time in a range of about 10 microsec to about 10 msec.

6. (Original) The method of claim 4, wherein a dosage of the gallium ions is in a range of about 1013 to about 1016 gallium ions per cm2.

7. (Original) The method of claim 1, wherein the nucleation site comprises a spot formed on the substrate, and the diameter of the spot is less than about 80 nm.

8. (Original) The method of claim 1, further comprising annealing the substrate after implantation.

9. (Original) The method of claim 8, wherein the annealing is performed at a temperature in the range of about 550 °C to about 750 °C.

10. (Original) The method of claim 1, wherein the substrate is a Si substrate.

11. (Original) The method of claim 10, wherein the step of growing a quantum dot on the nucleation site comprises growing a Ge island on the Si substrate by strained layer epitaxy.

12. (Original) The method of claim 11, wherein the Ge island is grown by introducing digermane gas onto the substrate at a substrate temperature in a range of about 550 °C to about 650 °C and digermane pressure in a range of about 10-8 Torr to about 10-6 Torr.

13. (Original) The method of claim 1, further comprising encapsulating the quantum dot.

14. (Original) The method of claim 13, wherein the step of encapsulating comprises forming an overgrowth layer over the substrate and the quantum dot.

15. (Original) The method of claim 1, further comprising:

prepatterning the substrate to form at least one prepatterned area.

16. (Original) The method of claim 15, wherein the location of the nucleation site is determined based on the at least one prepatterned area.

17. (Currently Amended) A method of forming a semiconductor device, comprising:

forming a nucleation site at a predetermined area of a semiconductor device layer by implantation with ions using a focused ion beam wherein an electronic microscope is used to align said ion beam on said predetermined area, the nucleation site comprising at least one surface or subsurface defect at the predetermined area ; and

growing a quantum dot on the nucleation site.

18. (Original) The method of claim 17, wherein the quantum dot is formed on the nucleation site by strained layer epitaxy.

19. (Original) The method of claim 17, wherein the semiconductor device is an optoelectronic device.

20. (Canceled)

21. (Currently Amended) The method of claim 17 ~~20~~, wherein the ions are selected from the group consisting of gallium, silicon and gold ions.

22. (Original) The method of claim 21, wherein the gallium ions are implanted using a beam energy in a range of about 1 keV to about 50 keV, a beam current of about 10pA, and an exposure time in a range of about 10 microsec to about 10 msec.

23. (Original) The method of claim 21, wherein a dosage of the gallium ions is in a range of about 1013 to about 1016 gallium ions per cm2.

24. (Original) The method of claim 17, wherein the nucleation site comprises a spot formed on the semiconductor device layer, and the diameter of the spot is less than about 80 nm.

25. (Original) The method of claim 17, further comprising annealing the semiconductor device layer after implantation.

26. (Original) The method of claim 25, wherein the annealing is performed at a temperature in the range of about 550 °C to about 750 °C.

27. (Original) The method of claim 17, wherein the substrate is a Si substrate and the step of growing a quantum dot on the nucleation site comprises growing a Ge island on the Si substrate by strained layer epitaxy.

28. (Original) The method of claim 27, wherein the Ge island is grown by introducing digermane gas onto the substrate at a substrate temperature in a range of about 550 °C to about 650 °C and digermane pressure in a range of about 10-8 Torr to about 10-6 Torr.

29. (Original) The method of claim 17, further comprising encapsulating the quantum dot.

30. (Original) The method of claim 29, wherein the step of encapsulating comprises forming an overgrowth layer over the semiconductor device layer and the quantum dot.

31. (Original) The method of claim 17, further comprising:

prepatterning the semiconductor device layer to form at least one prepatterned area.

32. (Original) The method of claim 31, wherein the location of the nucleation site is determined based on the at least one prepatterned area.